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APPLICANTS

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** CONTINUING DATA *****
 This application is a DIV of 10/007,384 10/22/2001 ABN *WLG*

** FOREIGN APPLICATIONS *****
 JAPAN P 2000-372228 12/07/2000 *WLG*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>WLG</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 2
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TITLE
 Manufacturing method for a semiconductor device with reduced local current

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